

ABSTRACT OF THE DISCLOSURE

A microcomputer comprises an intermittent operation control section for intermittently operating a CPU. Moreover, the microcomputer comprises a timer block, a level detecting circuit or a timer interlocking control section as the circuits for assisting operation of the CPU. The CPU realizes a timer function with the timer block. The CPU can start the process in response to a level of an external signal with the level detecting circuit. The CPU can start operation with the timer interlocking control section after an external apparatus is driven. With the structure explained above, power consumption of the microcomputer is reduced.